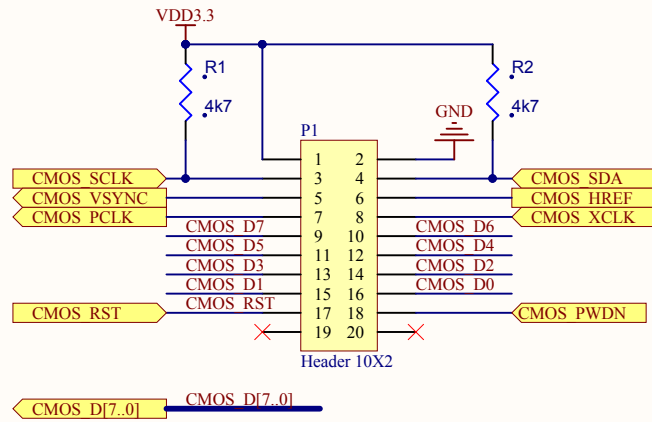
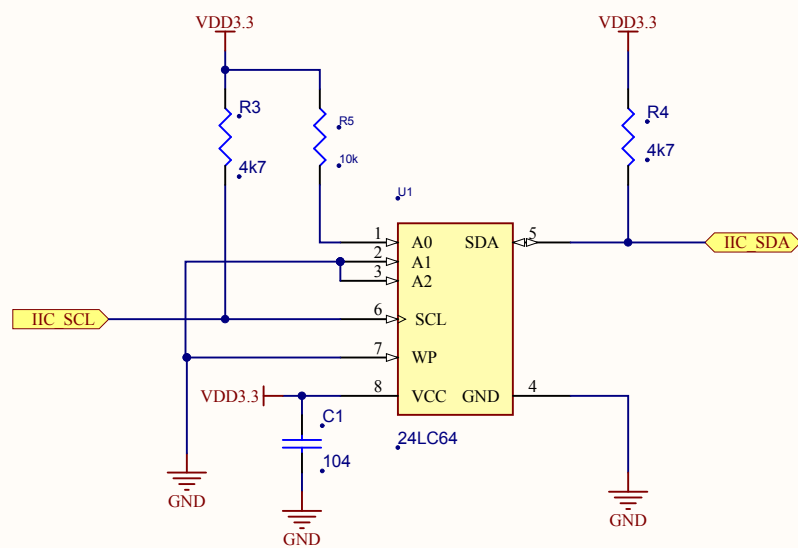
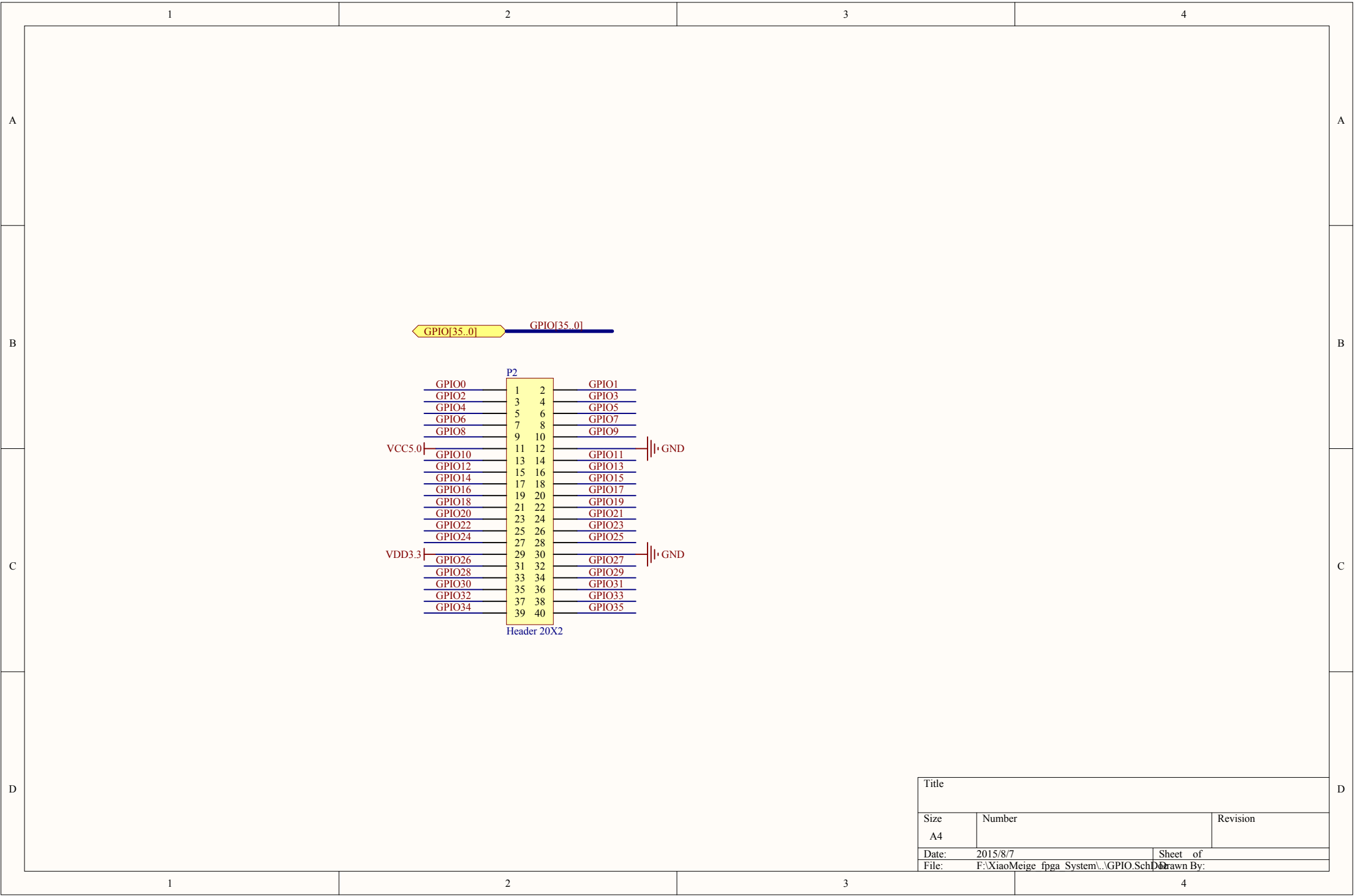




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Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
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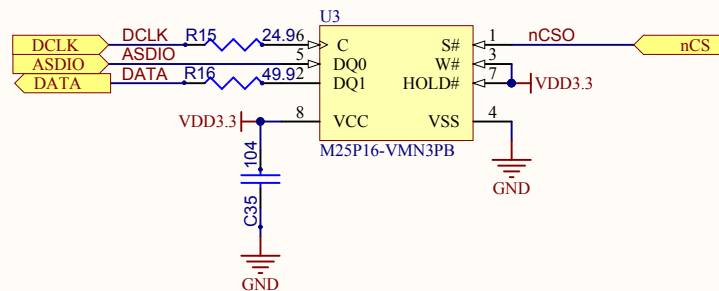


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Date:	2015/8/7	Sheet of
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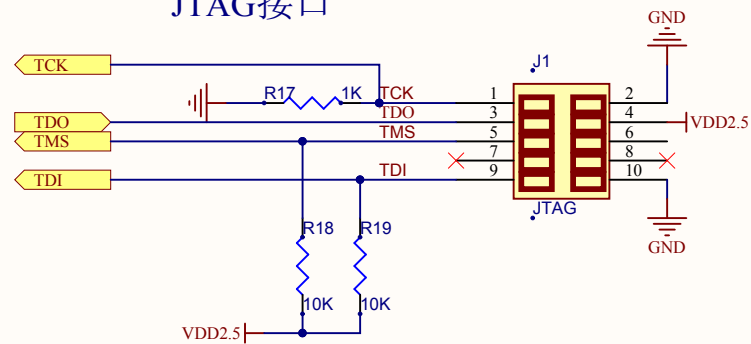


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Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
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	Drawn By:	

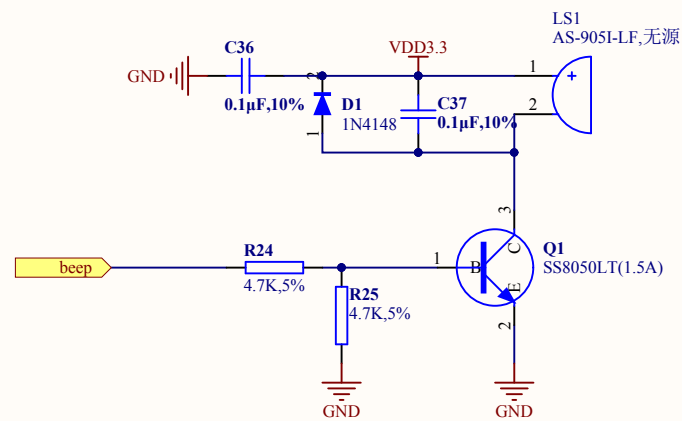
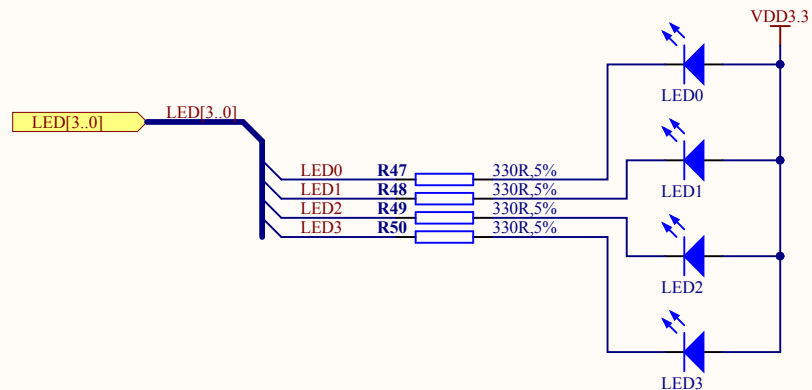
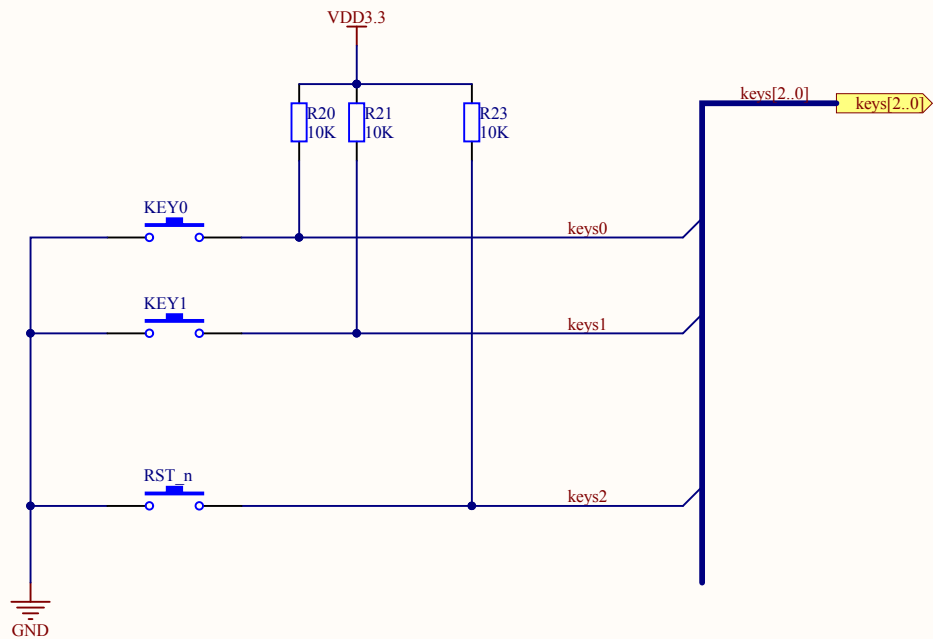
16Mbit串行FLASH(EPCS16)



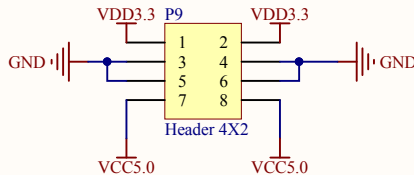
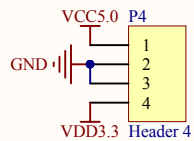
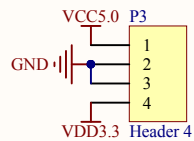
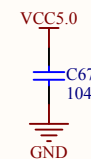
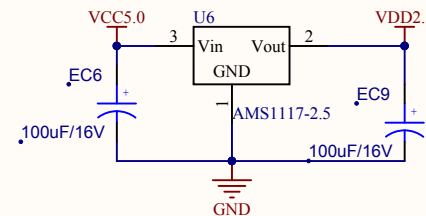
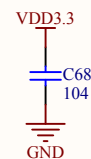
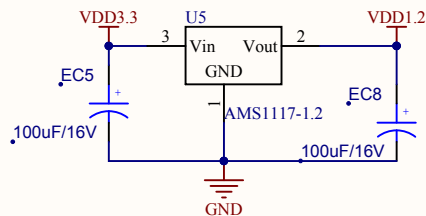
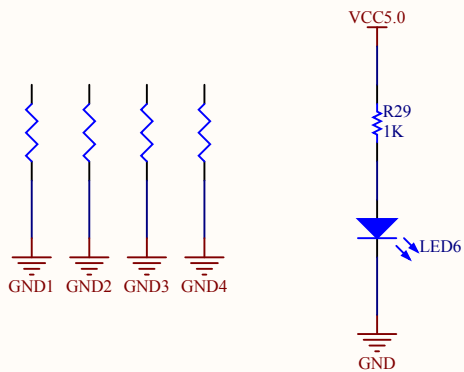
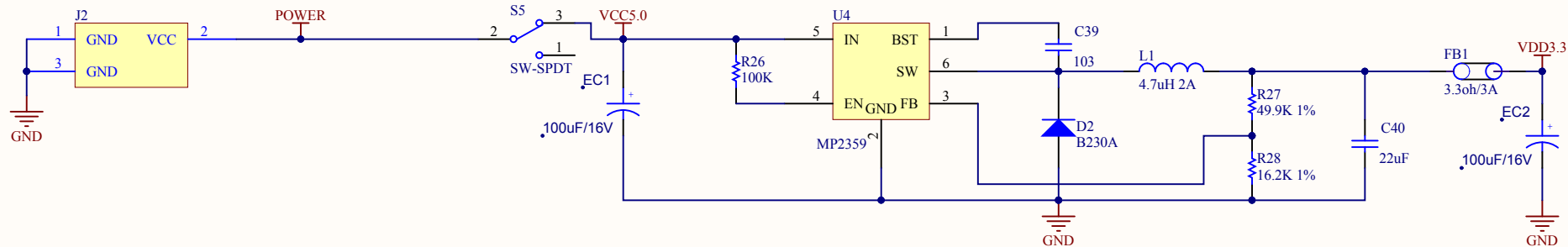
JTAG接口



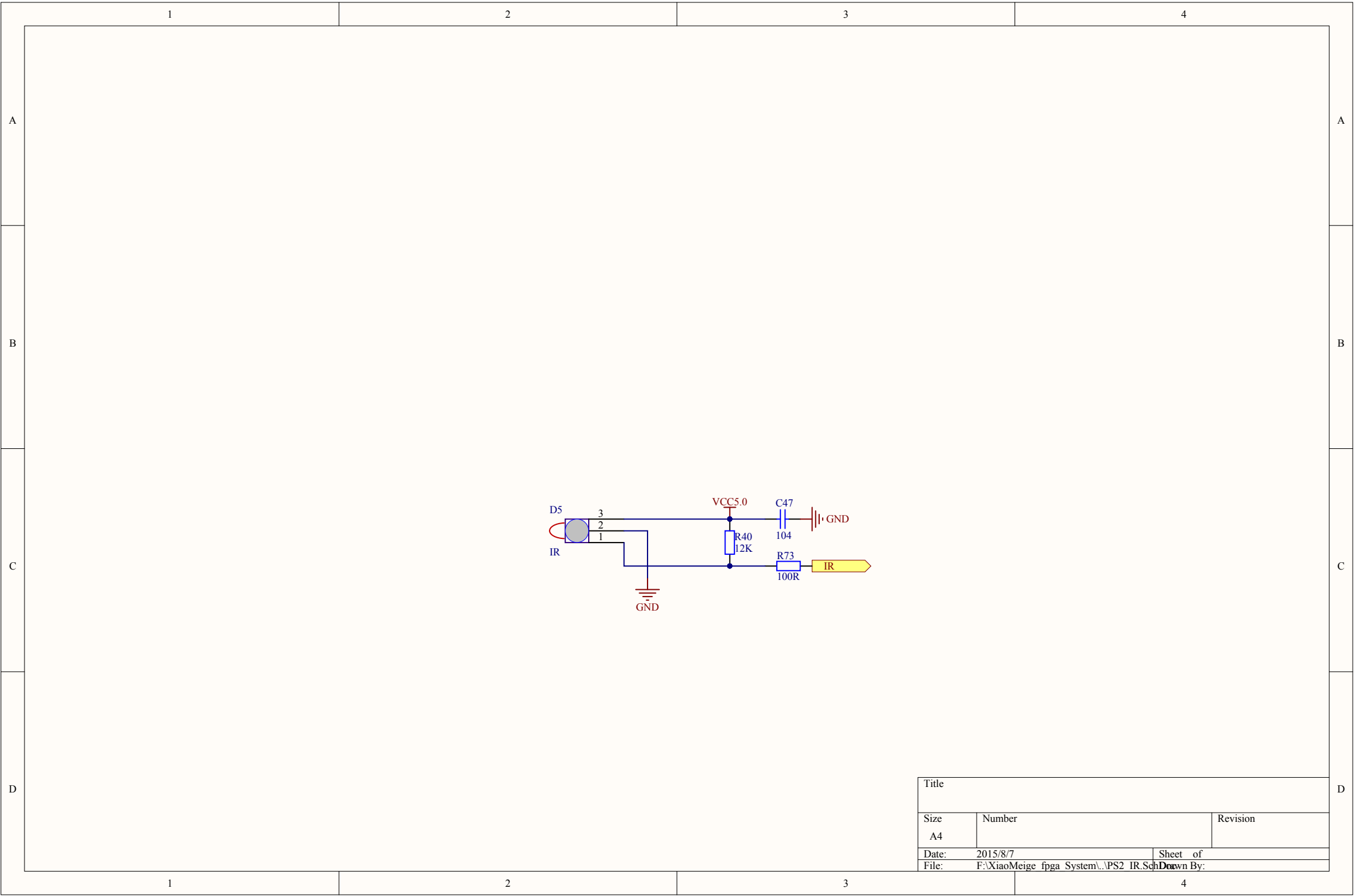
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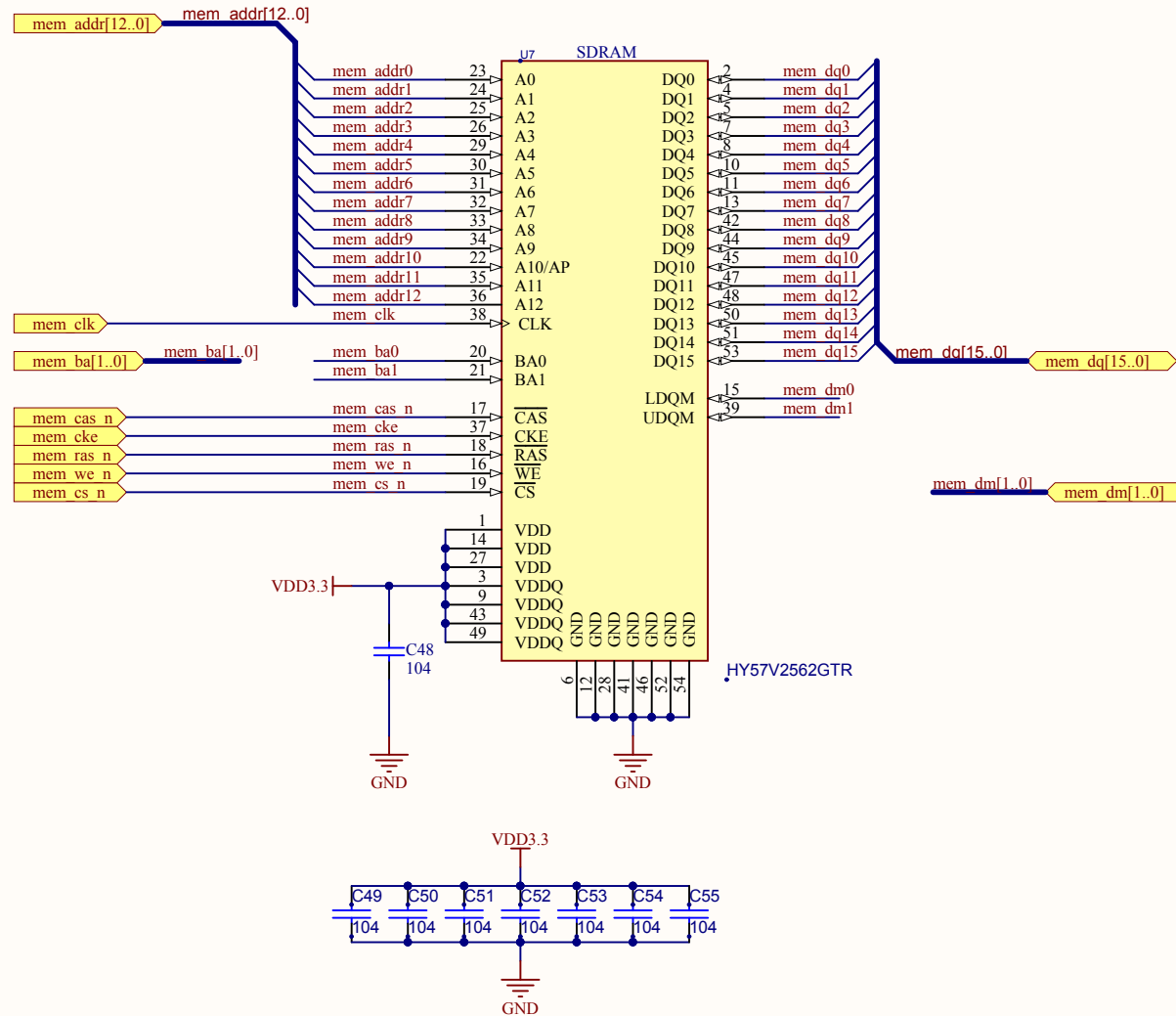
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A4		
Date:	2015/8/7	Sheet of
File:	F:\XiaoMeige fpga System\key4 led4 beep.doc	10 of 10



Title		
Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
File:	F:\XiaoMeige fpga System\POWER.SchDoc	
	Download By:	



256Mbit SDRAM



Title		
Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
File:	F:\XiaoMeige fpga System\SDRAM.Schematic	

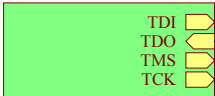
XTAL
XTAL.SchDoc



key4_led4_beep
key4_led4_beep.SchDoc



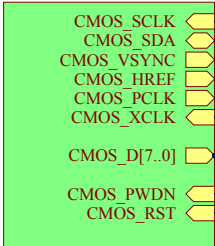
JTAG
JTAG.SchDoc



PS2_IR
PS2_IR.SchDoc



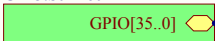
Camera
Camera.SchDoc



EEPROM
EEPROM.SchDoc



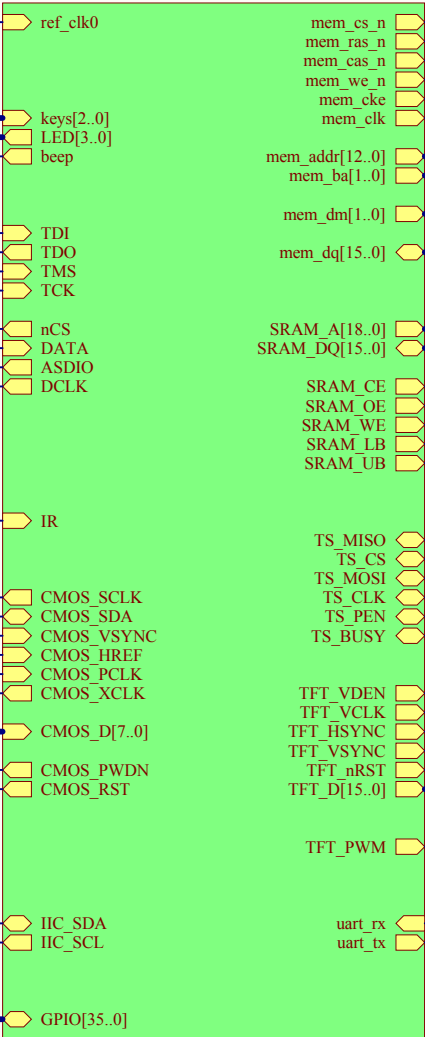
GPIO
GPIO.SchDoc



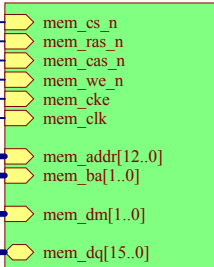
POWER
POWER.SchDoc



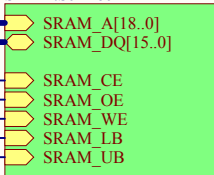
FPGA
FPGA.SchDoc



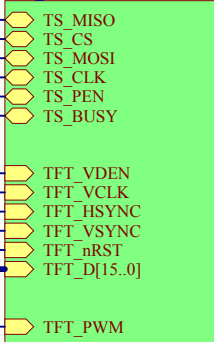
SDRAM
SDRAM.SchDoc



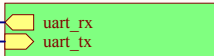
SRAM
SRAM.SchDoc



TFT_LCD
TFT_LCD.SchDoc



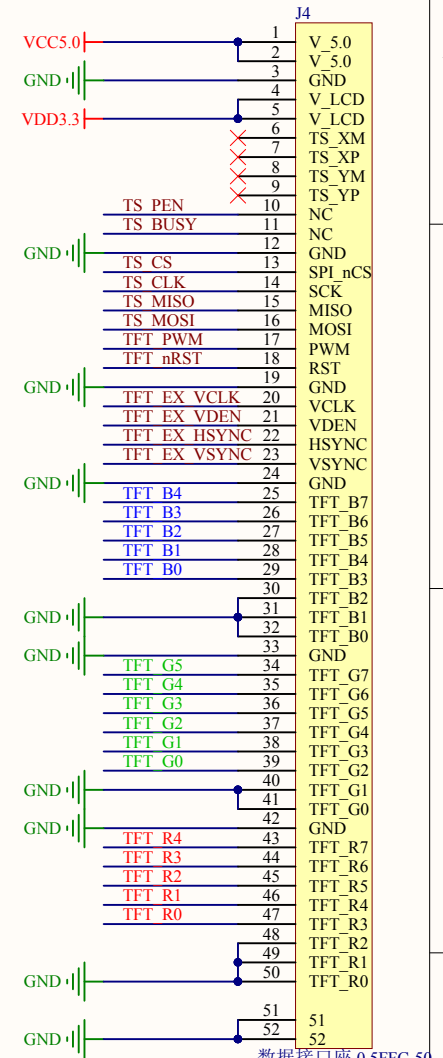
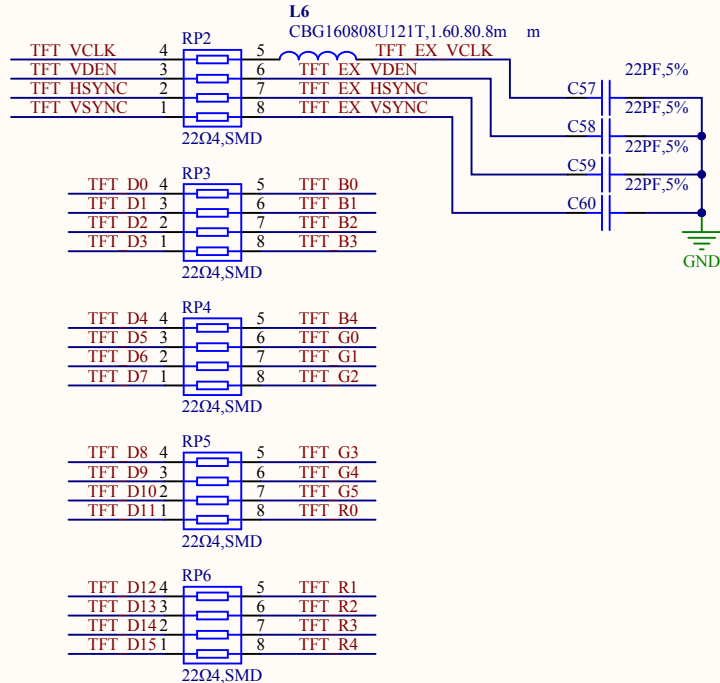
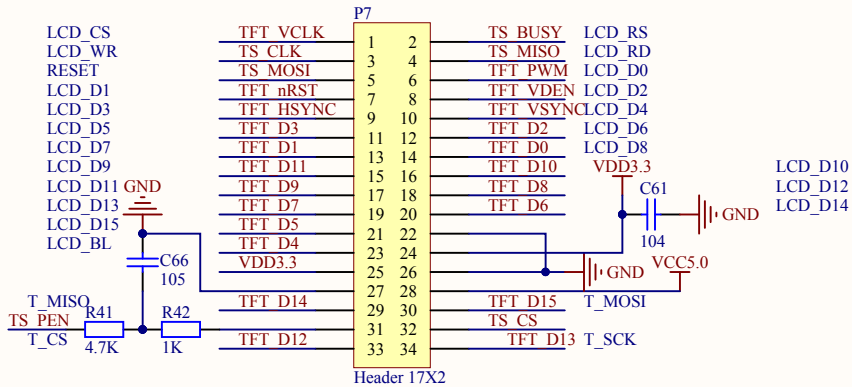
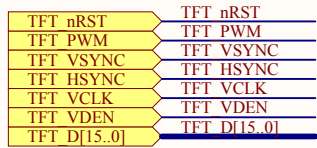
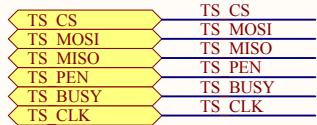
UART
UART.SchDoc



CMOS 17 pin
PS2_IR 3 pin
JTAG 4 pin
key_led_beep 9 pin
XTAL 1 pin
clk 2 pin
uart 2 pin
gpio 36 pin
TFT 28 pin
SRAM 39 pin
SDRAM 39 pin

刚好180个pin

Title		
Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
File:	F:\XiaoMeige fpga System\sys top.SchDoc	



此接口为扩展显示接口，方便扩展VGA，并支持正点原子的液晶屏

Title		
Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
File:	F:\XiaoMeige fpga System\TFT LCD_SchDoc	By:

1

2

3

4

A

A

B

B

C

C

D

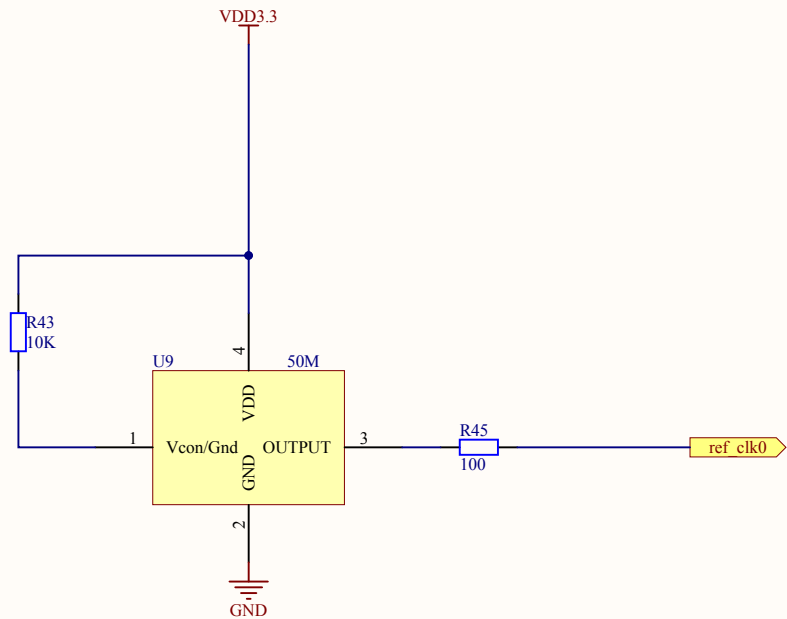
D

1

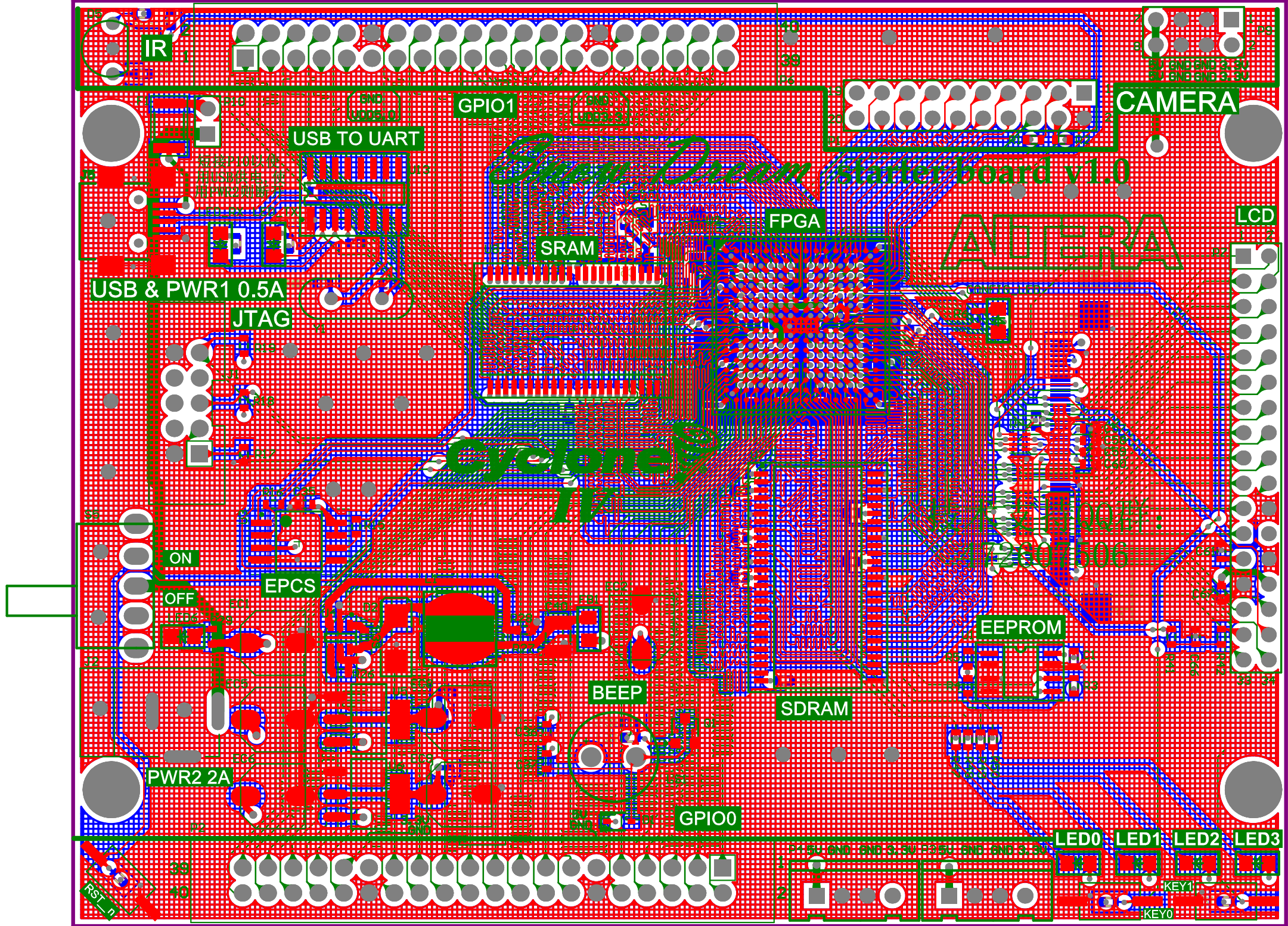
2

3

4



Title		
Size	Number	Revision
A4		
Date:	2015/8/7	Sheet of
File:	F:\XiaoMeige fpga System\...\XTAL.SchDoc	Drawn By:



Bill of Materials

fpga助学开发板

Source Data From: xiaomelge_fpga_PrlPcb
Project: xiaomelge_fpga_PrlPcb
Variant: None

Creation Date: 2015/8/7 9:39:14
Print Date: 42223 42223.4027

Footprint	Comment	LibRef	Designator	Description	Quantity
CAP0603	104	C_Dup4, Cap, Cap, Cap, Cap, C19, C20, C21, C22, C24, C26, C28, C30, Cap, Cap, Cap, C31, C32, C33, C35, C45, C46, C47, C48, Cap, Cap, Cap, C49, C50, C51, C52, C53, C54, C55, C56, Cap, Cap, Cap, C61, C67, C68, Cap, Cap, Cap, Cap, Cap, Cap, Cap, Cap, Cap, C_Dup4, C, C, Cap, Cap, C_Dup2, C_Dup2, C_Dup2, C_Dup2, C_Dup2, C_Dup2, C_Dup2, C_Dup2, Cap, Cap, Cap, Cap	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C24, C26, C28, C30, C31, C32, C33, C35, C45, C46, C47, C48, C49, C50, C51, C52, C53, C54, C55, C56, C61, C67, C68	[NoValue], Capacitor, Ca	45
CAP0603	0.1uF, 10%	1.02.17.0006	C36, C37	0.1uF, 10%, X7R,0603,RoHS	2
CAP0603	103	Cap	C39	Capacitor	1
C1210_N	22uF	Cap	C40	Capacitor	1
CAP0603	22PF, 5%	1.02.17.0005	C57, C58, C59, C60	22PF, 5%, 50V, COG,0603, RoHS	4
CAP0603	105	Cap	C66	Capacitor	1
CAP0603	22pF	CAP	C83, C84		2
LED-0805	CONFIG	LED_Dup1	CONFIG_LED		1
SOD323	DICDE1	1.04.07.0027	D1	1N4148,SOD-323,RoHS	1
SMB	8230A	Diode	D2	Default Diode	1
TO-92C-S	IR	IR	D5	红外接收头	1
AEC6.5	100uF/16V	EC	EC1, EC2, EC5, EC6, EC8, EC9		6
FUSE1812	Fuse	Fuse 2	F1	Fuse	1
ROM05	3.3oh/3A	C2hu	F81		1
Via	RES	RES	GND1, GND2, GND3, GND4		4
JTAG-10	JTAG	JTAG-10_Dup1	J1		1
POWER_CO	DC009电源插座	POWER_CON	J2		1
NL_max					
FPC-0.5-50P	FPC-50	1.10.01.0028	J4	数据接口座,0.5FPC-50,SMD,下接,RoHS	1
USB-MINI	USB_MINI	USB_MINI	J5		1
key_2	SW-PB	SW-PB	KEY1, KEY2, RST_n	Switch	3
电感7.5*7.5	4.7uH 2A	Inductor	L1	Inductor	1
CAP0603	INDUCTOR1	1.03.03.0007	L6	CB-G1608080U121T,1.6 0.8 0.8mm,0603,RoHS	1
LED-0805	LED	LED	LED0, LED1, LED2, LED3, LED6		5
LED-0805	green,0805	LED	LED_RX LED_TX		2
BUZZER2	1.06.09.0016	LS1	AS-9051-LF,无源,Φ9 4.8mm,潮州立		1
HDR2X10	Header 10X2	Header 10X2	P1	Header, 10-Pin, Dual row	1
IDC-40J	Header 20X2	Header 20X2	P2, P6	Header, 20-Pin, Dual row	2
SIP-A04	Header 4	Header 4	P3, P4	Header, 4-Pin	2
HDR2X17	Header 17X2	Header 17X2	P7	Header, 17-Pin, Dual row	1
HDR2X4	Header 4X2	Header 4X2	P9	Header, 4-Pin, Dual row	1
HDR1X2	Header 2	Header 2	P10	Header, 2-Pin	1
SOT-23	NPN-1	1.04.09.0001	Q1	SS8050L,T(1.5A),SOT-23,江苏长电,RoHS	1
RES0603	4k7	R_Dup6	R1, R2, R3, R4		4
RES0603	10k	R_Dup4, Res1, Res1, R_Dup2, R_Dup2, Res2, Res2, Res2	[NoValue], Resistor, Resistor, [NoValue], [NoValue], Resistor, Resistor, Resistor, Resistor		9
RES0603	330R	Res3	R6	Resistor	1
RES0603	10k 1%	Res1	R7, R10	Resistor	2
RES0603	24.9	R_Dup3	R15		1
RES0603	49.9	R_Dup3	R16		1
RES0603	1K	R_Dup2, Res1, Res2	R17, R29, R42	[NoValue], Resistor, Resistor	3
RES0603	4.7K, 5%	1.01.17.0019	R24, R25	4.7K, 5%,0603,RALEC,RoHS	2
RES0603	100K	Res1	R26	Resistor	1
RES0603	49.9K 1%	Res1	R27	Resistor	1
RES0603	16.2K 1%	Res1	R28	Resistor	1
RES0603	12K	Res2	R40	Resistor	1
RES0603	4.7K	Res2	R41	Resistor	1
RES0603	100	Res2	R45	Resistor	1
RES0603	330R, 5%	1.01.17.0019	R47, R48, R49, R50	330R, 5%,0603,RALEC,RoHS	4
RES0603	1K, 5%	1.01.17.0019	R51, R52	1K, 5%,0603,RALEC,RoHS	2
RES0603	100R	Res2	R13	Resistor	1
RESNET-1005X4(排阻)	22Ω 4,SMD	1.01.04.0010	RP2, RP3, RP4, RP5, RP6	22Ω 4,SMD,0402-8P4R,*,RoHS	5
SW-1 X 2	SW-SPDT	SW-SPDT	S5	SPDT Subminiature Toggle Switch, Right Angle Mounting, Vertical Actuation	1
24lc64	24LC64	24LC04B_0	U1		1
FBGA256	EP4ACE10F17C8	EP4ACE10F17C8	U2	Cyclone IV Family FPGA, 2V Core, 179 I/O Pins, 2 PLLs, 256-Pin FBGA, Speed Grade 8, Commercial Grade	1
epcs4	M25P16-VMN3PB	M25P16-VMN3PB	U3	16 Mb (x1) Automotive Serial NOR Flash Memory, 75 MHz, 2.7 to 3.6 V, 8-pin SO8 Narrow (MN), Tube	1
SOT23-6	MP2359	MP2359	U4		1
SOT-223	AMS1117-1.2	AMS1117	U5		1
SOT-223	AMS1117-2.5	AMS1117	U6		1
TSOP80P117 6-54(SDRAM)	HY57V2562GTR	HY57651620S_O_0	U7		1
TSOP80P117 6-44(SL25616)	IS61LV25616	IS61LV25616	U8		1
EXATL	TCXO	TCXO	U9		1
SOP16M	CH340G	CH340G	U13	USB2UART	1
CRYSTAL_DI	XATL	XATL	Y1		1
P - 0					151

Approved	Notes