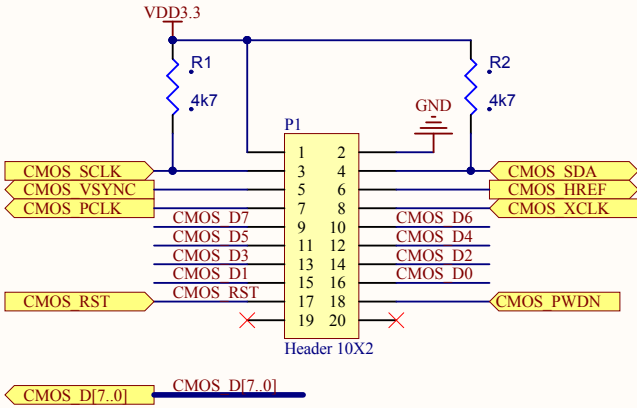
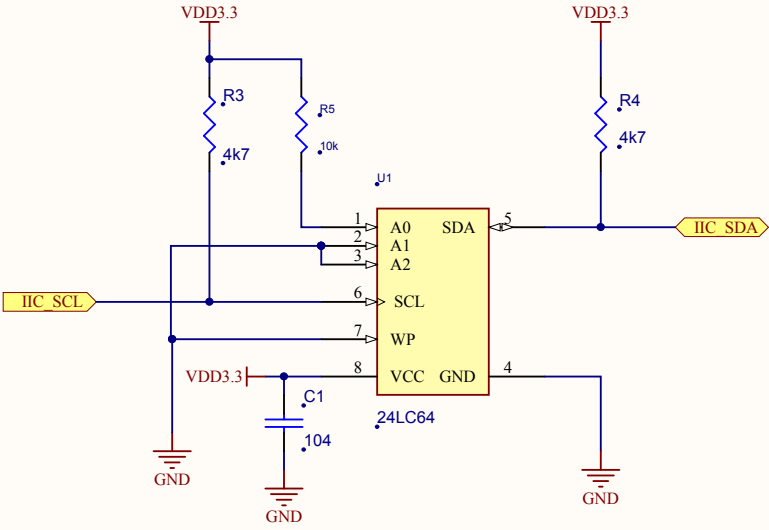


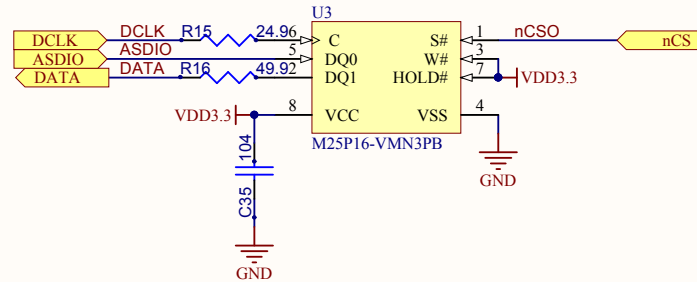


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Size	Number		Revision
A4			
Date:	2016/1/7	Sheet	of
File:	G:\XiaoMeige_fpga_System\...\Camera_SchDown By:		

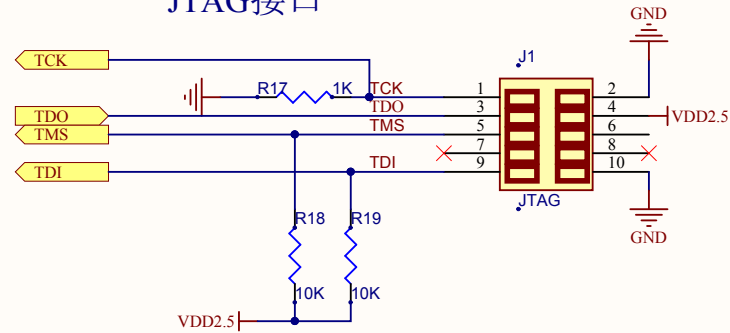


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A4			
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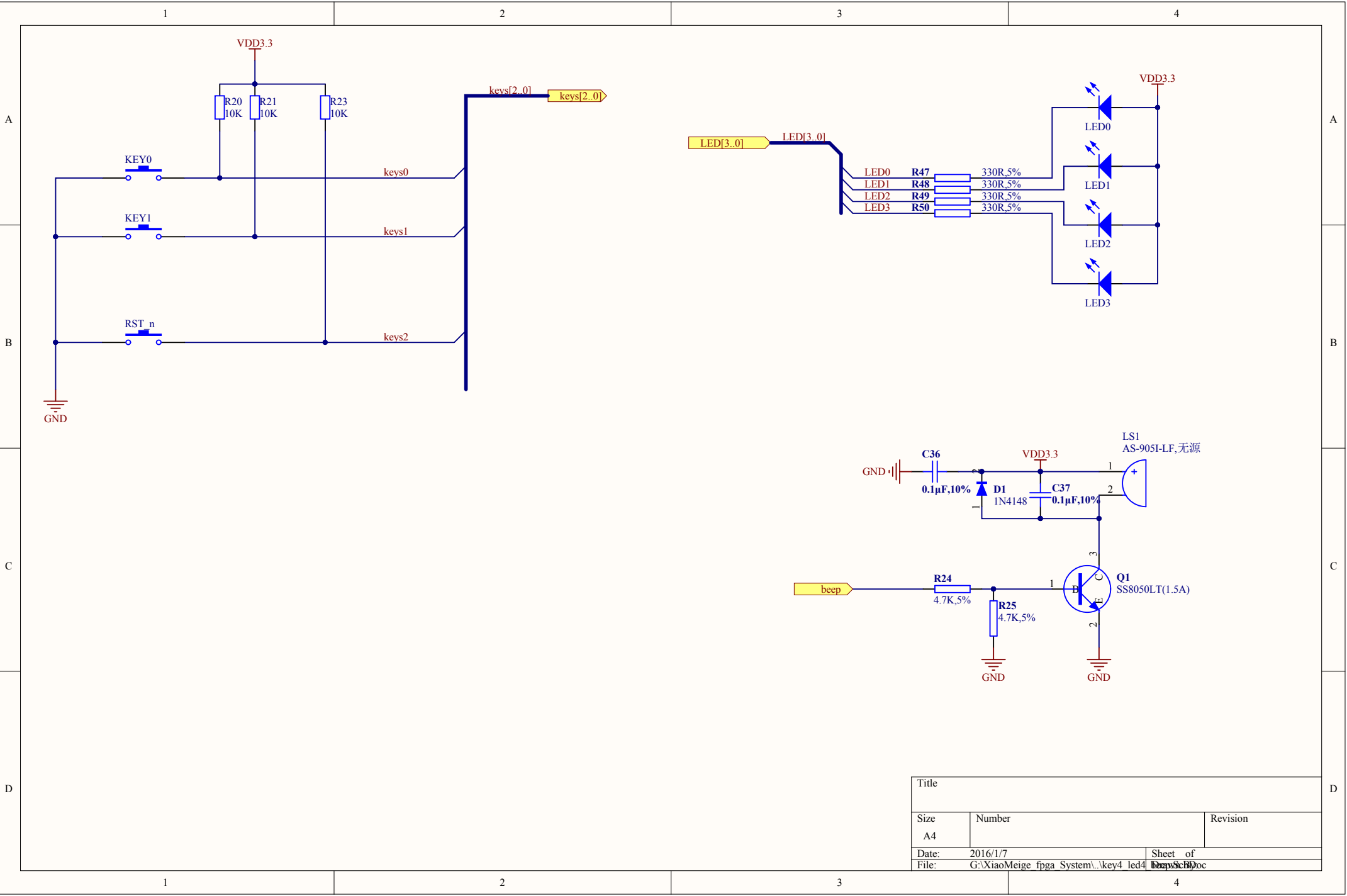
16Mbit串行FLASH(EPCS16)



JTAG接口



Title			
Size	Number		Revision
A4			
Date:	2016/1/7	Sheet	of
File:	G:\XiaoMeige_fpga_System\...\JTAG.SchDoc		



Title		
Size	Number	Revision
A4		
Date:	2016/1/7	Sheet of
File:	G:\XiaoMeige fpga System\key4 led4	4

1

2

3

4

A

A

B

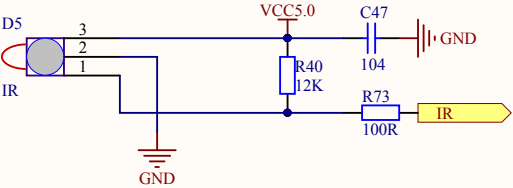
B

C

C

D

D



Title		
Size	Number	Revision
A4		
Date:	2016/1/7	Sheet of
File:	G:\XiaoMeige_fpga_System\...\PS2_IR_SchDown By:	

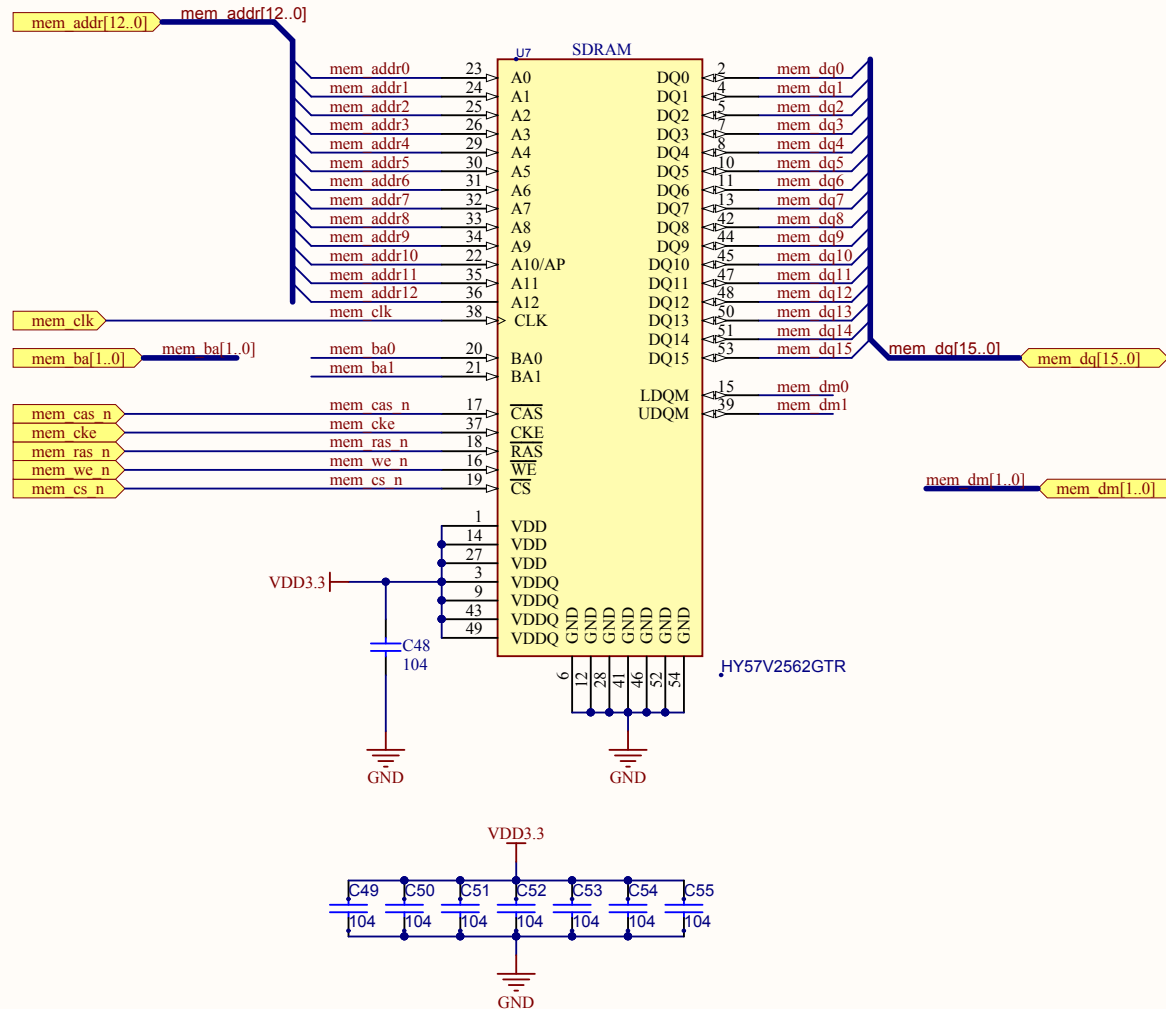
1

2

3

4

256Mbit SDRAM



Title		
Size	Number	Revision
A4		
Date:	2016/1/7	Sheet of
File:	G:\XiaoMeige fpga System\...\SDRAM.SchDoc	Drawn By:

XTAL
XTAL.SchDoc



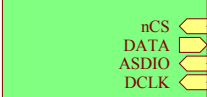
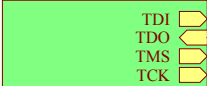
key4_led4_beep

key4_led4_beep.SchDoc



JTAG

JTAG.SchDoc



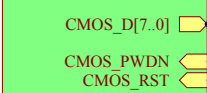
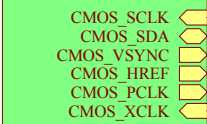
PS2_IR

PS2_IR.SchDoc



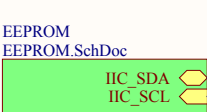
Camera

Camera.SchDoc



EEPROM

EEPROM.SchDoc

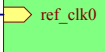


GPIO

GPIO.SchDoc



FPGA
FPGA.SchDoc



keys[2..0]

LED[3..0]

beep

TDI

TDO

TMS

TCK

nCS

DATA

ASDIO

DCLK

IR

CMOS_SCLK

CMOS_SDA

CMOS_VSYNC

CMOS_HREF

CMOS_PCLK

CMOS_XCLK

CMOS_D[7..0]

CMOS_PWDN

CMOS_RST

IIC_SDA

IIC_SCL

GPIO[35..0]

ref_clk0

keys[2..0]

LED[3..0]

beep

TDI

TDO

TMS

TCK

nCS

DATA

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TCK

nCS

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DCLK

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CMOS_SCLK

CMOS_SDA

CMOS_VSYNC

CMOS_HREF

CMOS_PCLK

CMOS_XCLK

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CMOS_PWDN

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GPIO[35..0]

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TDO

TMS

TCK

nCS

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ASDIO

DCLK

IR

CMOS_SCLK

CMOS_SDA

CMOS_VSYNC

CMOS_HREF

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CMOS_XCLK

CMOS_D[7..0]

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beep

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TMS

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nCS

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DCLK

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CMOS_SCLK

CMOS_SDA

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beep

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TDO

TMS

TCK

nCS

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ASDIO

DCLK

IR

CMOS_SCLK

CMOS_SDA

CMOS_VSYNC

CMOS_HREF

CMOS_PCLK

CMOS_XCLK

CMOS_D[7..0]

CMOS_PWDN

CMOS_RST

IIC_SDA

IIC_SCL

A

B

C

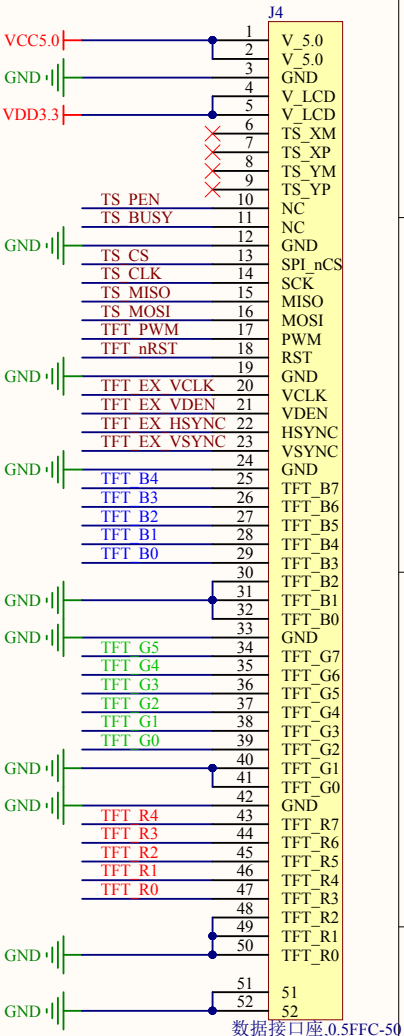
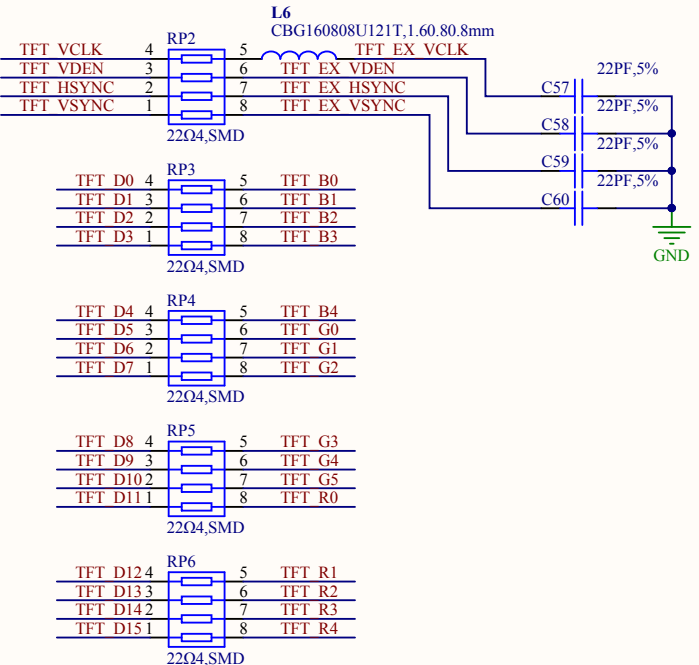
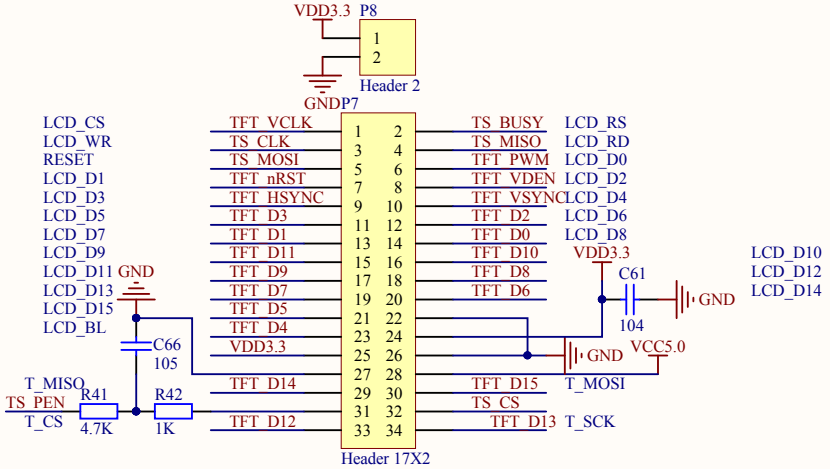
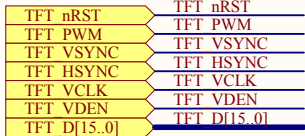
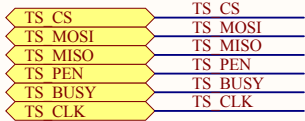
D

A

B

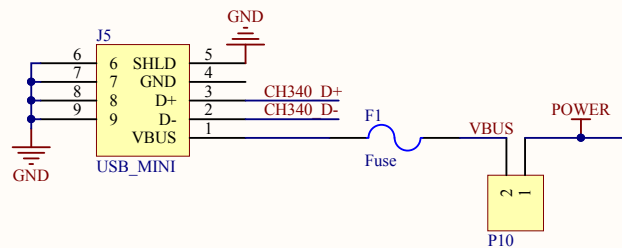
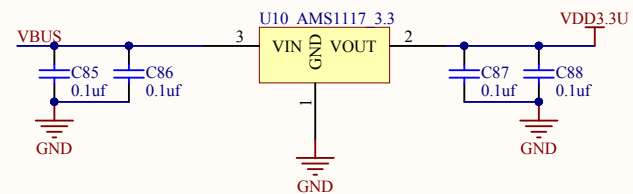
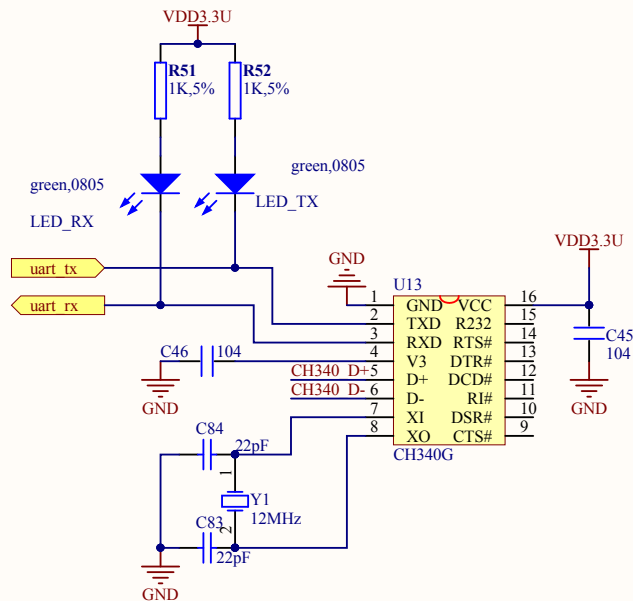
C

D

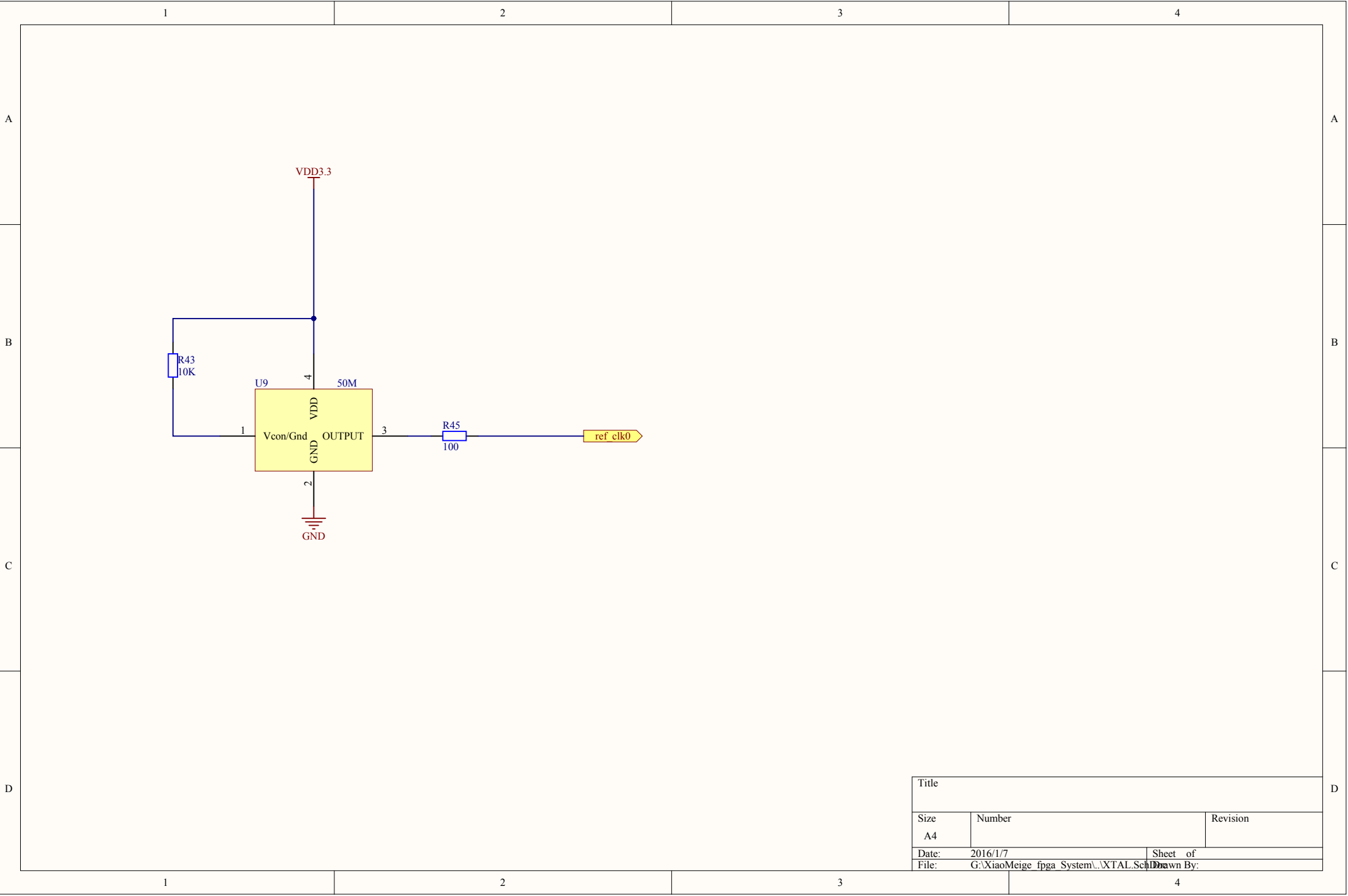


此接口为扩展显示接口，方便扩展VGA，并支持正点原子的液晶屏

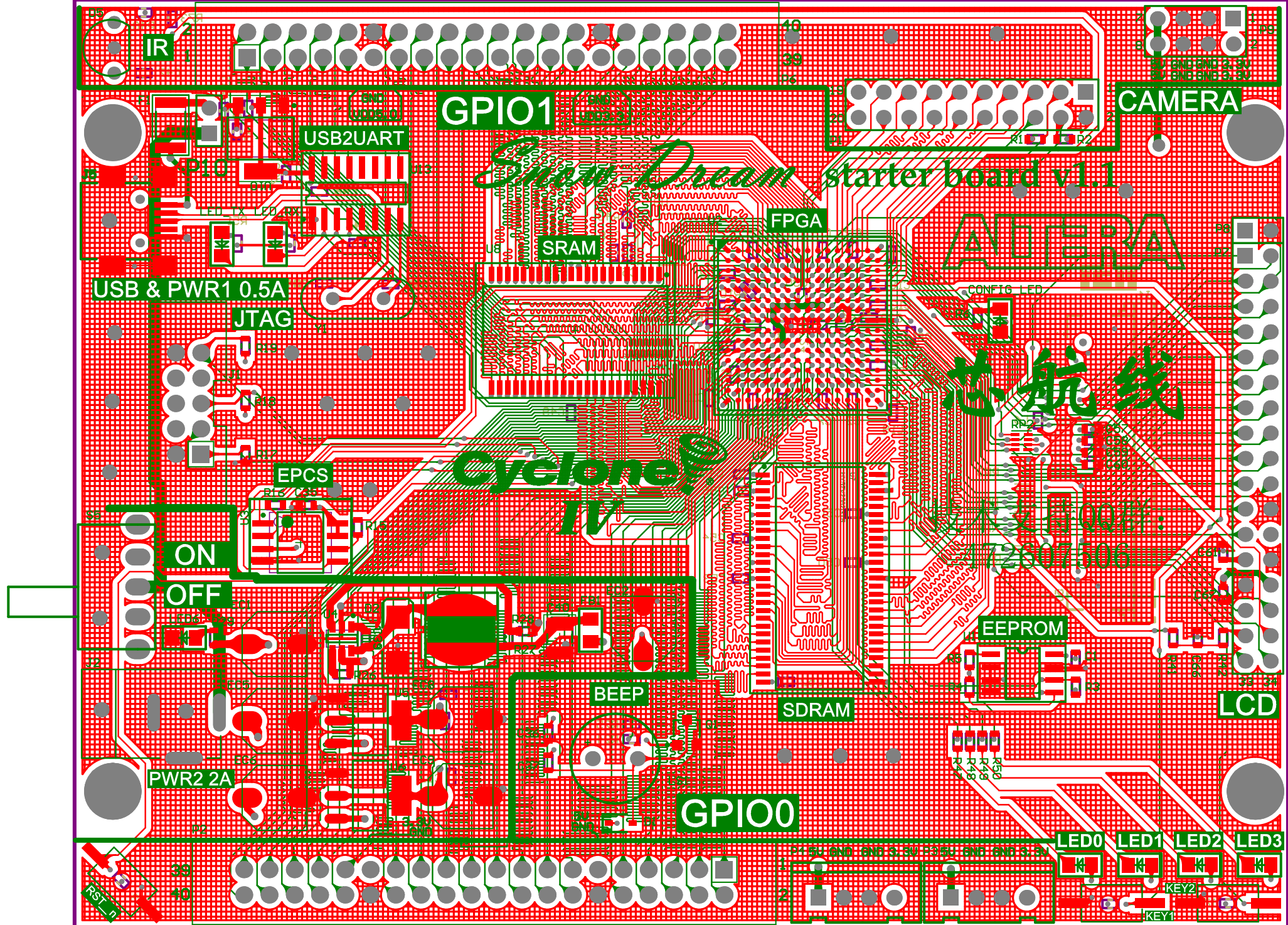
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File:	G:\XiaoMeige_fpga_System\TFT_LCD_SchDocBy:	



Title		
Size A4	Number	Revision
Date: File:	2016/1/7 G:\XiaoMeige fpga System\UART.Sch	Sheet of Drawn By:



Title			
Size	Number		Revision
A4			
Date:	2016/1/7	Sheet	of
File:	G:\XiaoMeige fpga System\...\XTAL.Sch		
		Drawn By:	



GPIO1

CAMERA

USB2UART

Snow Dream starter board v1.1

FPGA

SRAM

ALTERA

USB & PWR1 0.5A

JTAG

EPCS

Cyclone IV

ON

OFF

PWR2 2A

BEEP

SDRAM

EEPROM

LCD

GPIO0

LED0

LED1

LED2

LED3

KEY2

KEY1

芯航线

大友用QQ群:
72807506

Bill of Materials

fpga助学开发板

Source Data From:	<u>xiaomeige_fpga.PrjPcb</u>
Project:	<u>xiaomeige_fpga.PrjPcb</u>
Variant:	<u>None</u>

Creation Date:	2016/1/7	14:43:13
Print Date:	42376	42376.61347

Footprint	Comment	LibRef	Designator	Description	Quantity
CRYSTL_D1 P-0	12MHz	XATL	Y1		1
USB-MINI	USB_MINI	USB_MINI	J5		1
P-WALL	TCKO	TCKO	U8		1
SW-1 X 2	SW-SPDT	SW-SPDT	K5	SPDT Switch Toggle Switch, Right	1
key_2	SW-PB	SW-PB	RS0, KEY1, RST_n	Angle Mounting, Vertical Actuation	3
RE50603	47k	R_Dupd	R1, R2, R3, R4	(NoValue), Resistor, Resistor, (NoValue)	4
RE50603	10k	R_Dupd, Res1, Res1, R_Dupd, R_Dupd, Res2, Res2, Res2,	RS, RB, RF, R18, R19, R20, R21, R23, R43	(NoValue), Resistor, Resistor, Resistor, Resistor	4
RE50603	24 9	Res2 R_Dupd3	R15		1
RE50603	49 9	R_Dupd3	R17, R29, R42	(NoValue), Resistor, Resistor	3
RE50603	1K	R_Dupd, Res1, Res2			
RE50603	330R	Res3	R6	Resistor	1
RE50603	100	Res2	R75	Resistor	1
RE50603	100R	Res2	R33	Resistor	1
RE50603	12K	Res2	R60	Resistor	1
RE50603	4.7k	Res2	R41	Resistor	1
RE50603	100K	Res1	R26	Resistor	1
RE50603	10K 1%	Res1	RT, R10	Resistor	2
RE50603	10.2K 1%	Res1	R20	Resistor	1
RE50603	49.9K 1%	Res1	R27	Resistor	1
Via	RES		GND1, GND2, GND3, GND4		1
POWER_CON	DCK05电源插座	POWER_CON	J2		1
N_max					
SOT23-6	MP2359	MP2359	U3		1
epcs4	M25P16- VMM3PB	M25P16- VMM3PB	L4	16 Mb (x1) Automotive Serial NOR Flash Memory, 15 MHz, 2.7 to 3.6 V, 8-pin SO8 Narrow (NMN), Tube	1
LED-D0805	CONFIG	LED_Dup1	CONFIG_LED		1
LED-D0805	green,0805 LED	LED	RED, RED_LED_TX		2
LED-D0805	JTAG	JTAG-10_Dup1	LED0, LED1, LED2, LED3, LED5		5
TSOP80P117	IS61LV25616	IS61LV25616	U8		1
44(ISL25616	6				
IO-GC2-S	R	IR	D5	红外接收头	1
EM87 S7.5	A 7uH 2A	Inductor	L1	inductor	1
TSOP80P117	HYS7V2S62CR1	HYS7V5162DS	U7		1
6	O_0				
SA(SDRAM)					
DCX40J	Header 20X2	Header 20X2	P2_P8	Header, 20-Pin, Dual row	2
HDRX217	Header 17X2	Header 17X2	P7	Header, 17-Pin, Dual row	1
HDRX210	Header 10X2	Header 10X2	P9	Header, 10-Pin, Dual row	1
HDRX204	Header 4X2	Header 4X2	P3	Header, 4-Pin, Dual row	1
SIPAD4	Header 4	Header 4	P3_P4	Header, 4-Pin	2
FUSE1812	Fuse	Fuse 2	P8_P10	Header, 2-Pin	1
FPGA256	EPF10K10(7FC7)	EPF10K10(7FC7)	U2	Fuse	1
			C01	Cyclone IV Family FPGA, 2V Core, 170 I/O Pins, 2.55-Pin FBGA, Speed Grade B, Commercial Grade	1
AC06 S	100V/16W		E1, EC2, EC5, EC6, EC8, EC9		6
SMB	8204			Default Diode	1
CAP0603	C_Dupd, Cap.	C1, C2, C3, C4, C5, C6, C7, C8, C9, C10		(NoValue), Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor	45
	Cap, Cap, Cap.	C10, C20, C21, C22, C24, C26, C28, C30		Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor	
	Cap, Cap, Cap.	C31, C32, C33, C35, C45, C46, C47, C48		Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor	
	Cap, Cap, Cap.	C49, C50, C51, C52, C53, C54, C55, C56		Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor, Capacitor	
	Cap, Cap, Cap.	C61, C67, C68		Capacitor, Capacitor, Capacitor, Ca	
	Cap, Cap, Cap.				
	Cap, Cap, C, C.				
	Cap, Cap, C.				
	C_Dup2, C.				
	C_Dup2, C.				
	C_Dup2, C.				
	C_Dup2, C.				
	C_Dup2, C.				
	C_Dup2, Cap, Cap, Cap.				
RD085	3.3oh/3A	CD20u	FB1		1
SOP16M	CH340G	CH340G	U13	USB2UART	1
CAP0603	0.1uf	Cap Semi	C85, C86, C87, C88	Capacitor (Semiconductor SIM Model)	4
CAP0603	105	CAP	C30	Capacitor	1
CAP0603	22pf	CAP	C83, C84		2
CT120_N	22uf	CAP	C40		1
SOT-23	AMS1117-1.2	AMS1117	U5	Capacitor	1
SOT-23	AMS1117-2.5	AMS1117	U6		1
AMS1117-3.3	AMS1117-3.3	AMS1117	U10		1
24US4	24LC04E	24LC04E_0			1
FFC0-515P	FFC50	1 1.10 01.0028	24	数据接口座,0.5FFC-50,SMD,24Pin,1.27mm,1.	

Approved	Notes